

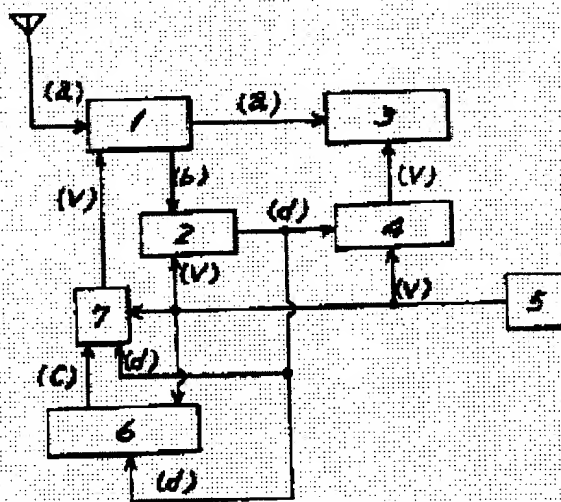
RECEIVER

Patent number: JP58105632
Publication date: 1983-06-23
Inventor: NAKAGAWA MIKIO
Applicant: MITSUBISHI DENKI KK
Classification:
- **International:** H04B1/16; H04B7/24
- **European:**
Application number: JP19810204670 19811217
Priority number(s):

Abstract of JP58105632

PURPOSE: To reduce power consumption by intermittently supplying electric power to a waiting circuit in waiting status.

CONSTITUTION: Square wave pulse signals are continuously applied from a monostable oscillating circuit 6 to a switching circuit 7 to intermittently drive the circuit 7. The circuit 7 supplies electric power to a low power circuit which operates in reception waiting status and actuates the circuit 1 only when the circuit 7 is on. At the reception of radio waves, a detection signal is outputted from a signal detecting circuit 2. The detection signal turns on a switching circuit 4 to actuate an ordinary receiving circuit. At the same time the operation of the monostable oscillating circuit 6 is stopped.



Data supplied from the esp@cenet database - Patent Abstracts of Japan